

Zagdu Singh Charitable 'Trust's (Regd.)

THAKUR COLLEGE OF **ENGINEERING & TECHNOLOGY**

(Approved by AICTE, Govt. of Maharashtra & Affiliated to University of Mumbai*) (Accredited Programmes by National Board of Accreditation, New Delhi**)

A - Block, Thakur Educational Campus, Shyamnarayan Thakur Marg, Thakur Village, Kandivali (East), Mumbai - 400 101.

Tel.: 6730 8000 / 8106 / 8107 Fax: 2846 1890 Email: tcet@thakureducation.org
Website: www.tcetmumbai.in • www.thakureducation.org



Revision: B

*Permanent Affiliated UG Programmes: • Computer Engineering • Electronics & Telecommunication Engineering • Information Technology (w.e.f.: A.Y.2015-16 onwards)

**1st time Accredited UG Programmes: • Computer Engineering • Electronics & Telecommunication Engineering • Information Technology

**2nd time Accredited UG Programmes: • Computer Engineering • Electronics & Telecommunication Engineering • Information Technology • Electronics Engineering (3 years w.e.f.: 01-07-2016)

Semester Plan (Practical / Tutorials / Assignment)

Semester: III Course: **B.E EXTC** Batches: S.E A1 & A2

A3 & A4

Subject: Electronic Instrumentation and Controls Class: S.E EXTC- A Batch size: 43 Students

Laboratory faculty in charge: Ms. Amruta Naik Lab. Assistant /Attendant:

Note: Tutorials planned as per University of Mumbai Curriculum

Basic Tutorials:

TCET/FRM/IP-02/10

Sr. No.	TITLES Experiments / Tutorials / Assignment (Planning with use of Technology)	Planned Date	Completion Date	Remarks
1.	Tutorial on calculation of transfer function for given practical system	18/07/17		
'.		27/07/17		
2.	Tutorial on Block Diagram Reduction	24/07/17		
	Tutoriai on Block Biagram Reduction	27/07/17		
3.	Tutorial on finding stability of given	31/07/17		
	system	03/08/17		
4.	Tutorial on Time domain analysis of	07/08/17		
	given system	10/08/17		
5.	Tutorial on Frequency domain analysis	14/08/17		
	of given system	31/07/17 03/08/17 07/08/17		
6.	Tutorial on basic measurement	04/09/17		
	specifications and on RLC measurement	31/08/17		
7.	Tutorial on working of transducers & sensors	11/09/17		
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		07/09/17		
		11/09/17		
8.	Tutorial on data acquisition system	07/09/17		
Desig	n/ Development Tutorials:			
9	Design based tutorial on Transient & steady state behaviour of first and second order systems	18/09/17		
9		14/09/17		
10	Design based tutorial on plotting frequency response using Bode plot,	25/09/17		
10	Polar plot, Nyquist plot	21/09/17		
	Total plot, 1 (yquist plot			
S.No.	Group Activity	Date	Modes of Learning	Recommended Sources
		Date 11-08-17		
S.No. 1.	Group Activity Assignment on Transfer function model of electrical systems & block diagram		Learning Self	Sources Resource Book



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ISO 9001:2008 Certified

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**2nd time Accredited UG Programmes : *Computer Engineering * Electronics & Telecommunication Engineering * Information Technology * Electronics Engineering (3 years w.e.f.: 01-07-2016)

	Planned	Completed		Planned	Completed		Planned	Completed
No. of Prac	NA		No. of Assign ments	03		No. of Tutorial	Basic Tuts: 08 Design Base Tuts: 02	
DOSLNE:				DOSI	DOSLE (engaged in some other dates):			

Group activities are required to be added with the practical related to course to enhance the learning activity of the student in the course. Group activity includes: Group presentation, new experiment design, mini projects et

tne student in the course. Group acti Note:	vity includes: Group presentatio	n, new experiment design, mini projects etc.
	n date shall be in compliance. For ar	ny non-compliance reason(s) required to be stated
be mapped at the end of the semeste	er.	ments/ tutorials/ assignments and are required to
3. Entry for DOSLE (engaged on some or	ther date) shall be done with proper	mapping to DOSLNE.
Sd/-	Sd/-	Sd/-
(Ms. Amruta Naik)	(Dr. Vinitkumar Dongre)	(Dr. R.R Sedamkar)
Name & Signature of Faculty	Signature of HOD	Signature of Principal / Dean Academic
Date: 21/07/2017	Date:	Date: